

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	66	("4814981"   "5251311"   "5386565"   "5423050"   "5434804"   "5440705"   "5448576"   "5452432"   "5455936"   "5467464"   "5479652"   "5483518"   "5488688"   "5495596"   "5509126"   "5530965"   "5570375"   "5590354"   "5596734"   "5598551"   "5606566"   "5608881"   "5613153"   "5627842"   "5657273"   "5682545"   "5704034"   "5708773"   "5724549"   "5737516"   "5751621"   "5768152"   "5771240"   "5774701"   "5778237"   "5781558"   "5796978"   "5828825"   "5832248"   "5835963"   "5838692"   "5848247"   "5860127"   "5862387"   "5867726"   "5884023"   "5884092"   "5896550"   "5918045"   "5930523"   "5930833"   "5944841"   "5950012"   "5953538"   "5956477"   "5978870"   "5978874"   "5978902"   "5983017"   "5983379"   "6014763"   "6041378"   "6125416"   "6178525"   "6279103"   "6282674").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/14 11:51
L4	659	emulat\$4 and packet\$1 and debug\$4 and @ad<"20000303"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/07/14 12:07
L5	615	L4 and processor	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/07/14 11:52
L6	2794	debug and trace	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/07/14 12:08
L7	84	debug and trace	IBM_TDB	OR	OFF	2005/07/14 13:17
L8	0	fpga and emulat\$4 and packet and trace	IBM_TDB	OR	OFF	2005/07/14 13:20
L9	0	703/26.ccls. and @ad<"20000303"	IBM_TDB	OR	OFF	2005/07/14 13:19
L10	248	703/26.ccls. and @ad<"20000303"	US-PGPUB; USPAT	OR	OFF	2005/07/14 13:25
L11	71	fpga and emulat\$4 and packet and trace	US-PGPUB; USPAT	OR	OFF	2005/07/14 13:20
L12	25	L10 and packet\$1	US-PGPUB; USPAT	OR	OFF	2005/07/14 13:25

S1	218	703/24.ccls. and @ad<"20000303"	US-PGPUB; USPAT	OR	OFF	2005/07/13 19:30
S2	1215	(714/724.ccls. or 714/45.ccls.) and @ad<"20000303"	US-PGPUB; USPAT	OR	OFF	2005/07/13 16:20
S3	131	703/28.ccls. and @ad<"20000303"	US-PGPUB; USPAT	OR	OFF	2005/07/13 16:12
S7	255	on-chip adj debug\$4	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/07/13 19:49
S8	76	S7 and @ad<"20000303"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/07/13 19:49

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AND

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AND

packet

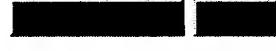
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space, involving perceptual experiences with **real** objects and physical tiredness, or the movement  
of interaction (as happens for example in the ILEX **system** [Knott et al. 1996 ])New hardware technology  
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ecate.itc.it:1024/petrelli/publications/HT97-final.ps.gz

[Compositional Modelling of Reflective Agents](#) - Brazier, Treur (1996) (Correct) (2 citations)

[Formal Specification of Multi-Agent Systems: a Real World Case](#), In: V. Lesser (Ed. Proc. First Int.  
This process, which may also be performed at run-time, results in the delegation of tasks to the  
compositional architectures for multi-agents **systems** is presented. A structure for reflective agents  
www.cs.vu.nl/~wai/pub/1996/Brazier\_Treur01.ps.Z

[A Unified Network-based Approach for Online Recognition of..](#) - Lee, Kim (Correct)

Several approaches are possible to develop a **system** which can recognize handwritten text in  
ai.kaist.ac.kr/~joony/ps/IWFHR\_96.ps

[Performance Comparison Of Video Transport Over ATM..](#) - Hossain, Kang, Horst (Correct)

transmitted over the link. It is very important to realize that these 64Byte packets are never lost in  
algorithm. That algorithm uses Round Trip Time (RTT) to the VS, client's consumption rate of  
SparcStation20s (SS20) and 100 MHz Pentium (P5) **systems** as video servers. Our study compares CPU  
berserk.vlsi.uiuc.edu/people/ashfaq/ieee.mm97.ps

[On the Optimality of the Simple Bayesian Classifier under..](#) - Domingos, Pazzani (1997) (Correct) (93 citations)  
the Bayesian classifier, efficiently verifiable on **real** problems. In Section 6 we began the work towards  
has many desirable properties (simplicity, low **time** and memory requirements, etc. and thus may well  
due to noise in the sample, the statistical bias (**systematic** component of the approximation error, or  
www.ics.uci.edu/~pedrod/mlj97.ps.gz

[Mobile Robot Localization with an Artificial Neural Network](#) - Racz, Dubrawski (Correct)

problem solution. The **system**, trained on a **real** world data of a door neighborhood region reveals  
as well as its updating routine, usually memory and **time** consuming, are both not neccesary to solve the  
for the considered problem solution. The **system**, trained on a **real** world data of a door  
www.cs.cmu.edu/afs/cs.cmu.edu/user/awd/www/irs94\_racz\_dubrawski.ps.gz

[Sequential Diagnosis of Double Regular Systems](#) - Boros, Ünlüyurt (1997) (Correct)

about k-out-of-n **systems**, we provide a polynomial **time** algorithm for the most cost-efficient sequential  
Sequential Diagnosis of Double Regular **Systems** Endre Boros y Tonguc Unluyurt y October  
new-rutcor.rutgers.edu/~amai/Extended\_Abstracts/TUnluyurt.ps

[Discovery of Numerical Dependencies in Form of Rational..](#) - Kiselev, Arseniev (Correct)

to discovery of numerical dependencies between **real** valued variables are at least as common for  
industry as classification problems. Until recent **times** the prevailing number of works in this field was  
part of problems solved by the machine discovery **system** PolyAnalyst belongs to this class [1, 2]  
www.megaputer.com/DOWN/PAismis6.PS

[MORPH: A System Architecture for Robust High Performance Using ..](#) - Chien, Gupta (1996) (Correct)  
(5 citations)

inquiry, complex **systems** design, faster than **real** life simulations, and higher fidelity human  
in similar fashion to SRAM, approximately four **times** for every three years. The routability and gate  
MORPH: A **System** Architecture for Robust High Performance Using  
www-csag.ucsd.edu/papers/csag/external/morph.ps

[A Bibliography of Papers in Lecture Notes in Computer Science..](#) - Beebe (1999) (Correct)

Read [1279, 2137, 2156]Read-Once [1279, 2137]Real [1164, 774, 886, 165, 646, 304, 708, 613, 2162,  
[2087]Discrete-Ordinates [509]Discrete-Time [1305, 2188]Discretization [2254, 511]



July 14, 2005

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